

**PATENT NUMBER and
ISSUE DATE**

UTILITY Patent Application

APPL NUM 10020961	FILING DATE 12/19/2001	CLASS 257	SUBCLASS 166	GAU 2815	EXAMINER ISAAC
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430 2812 JT

****CONTINUING DATA VERIFIED:**

****FOREIGN APPLICATIONS VERIFIED:**

JAPAN 2000-384543 12/19/2000

JAPAN 2000-403098 12/28/2000

PG-PUB	DO NOT PUBLISH <input type="checkbox"/>	RESCIND <input type="checkbox"/>	ATTORNEY DOCKET NO 740756-2410
Foreign priority claimed <input type="checkbox"/> yes <input type="checkbox"/> no		35 USC 119 conditions met <input type="checkbox"/> yes <input type="checkbox"/> no	
Verified and Acknowledged Examiners's initials			

TITLE: Method of manufacturing semiconductor device and semiconductor device

U.S. DEPT. OF COMM./PAT. & TM.-PTO-436L (Rev. 12-94)

NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED					
		Assistant Examiner	<table border="1"> <tr> <td>Total Claims</td> <td>Print Claim for O.G.</td> </tr> <tr> <td></td> <td></td> </tr> </table>	Total Claims	Print Claim for O.G.		
Total Claims	Print Claim for O.G.						
ISSUE FEE		DRAWING					
Amount Due	Date Paid	Sheets Drawn	Figs. Drawn				
Primary Examiner		Application Examiner					
<input type="checkbox"/> TERMINAL DISCLAIMER		PREPARED FOR ISSUE					
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